

Fig. 1

Master Controller

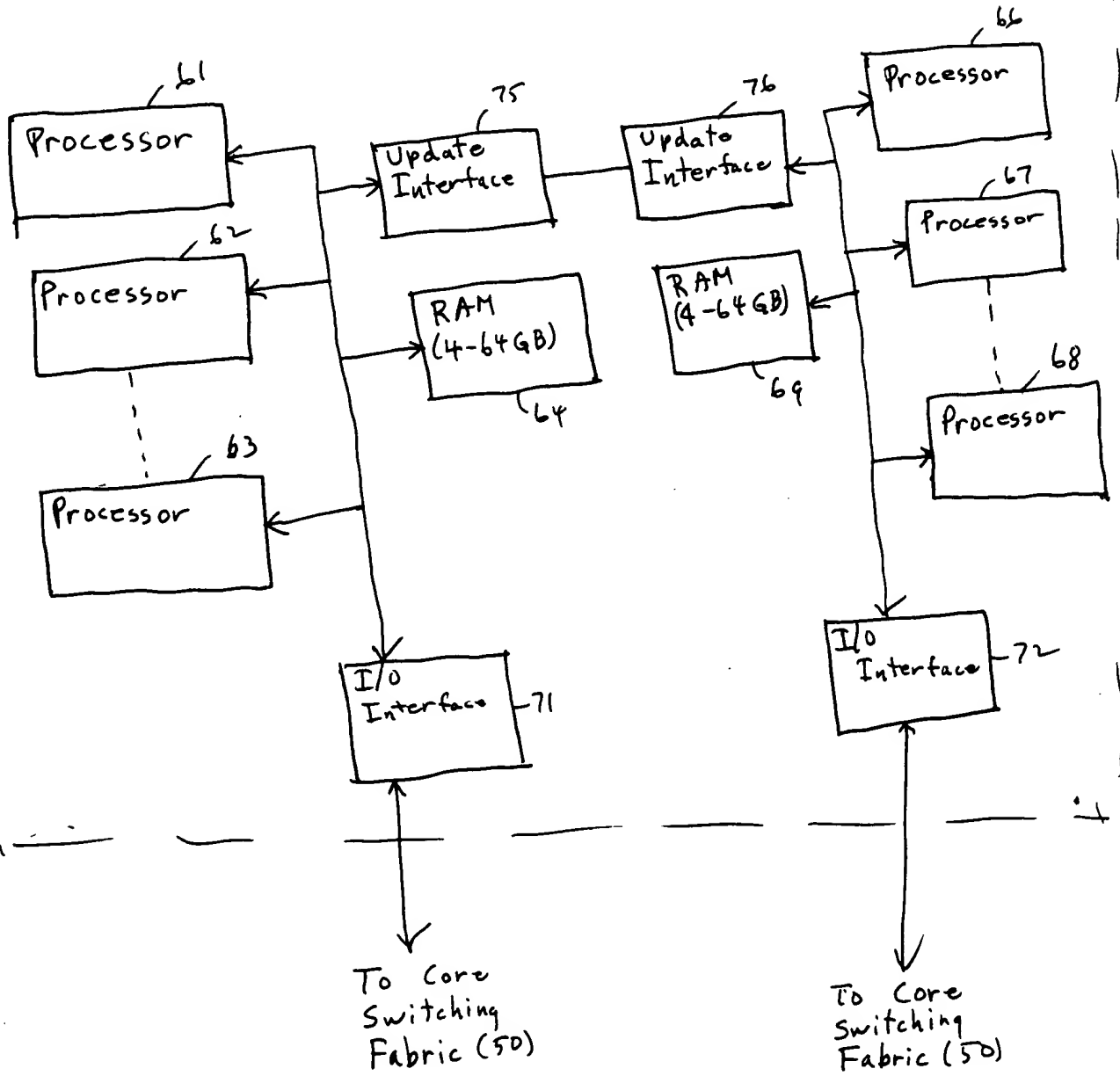


Fig. 2

